

# DSG-NPS R&D Meeting Minutes

**Date:** March 30, 2021

**Time:** 11:00AM – 12:00 PM

*Attendees: Mary Ann Antonioli, Peter Bonneau, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Marc McMullen, Tyler Lemon, and Amrit Yegneswaran*

## 1. HV Supply Cable fabrication and testing

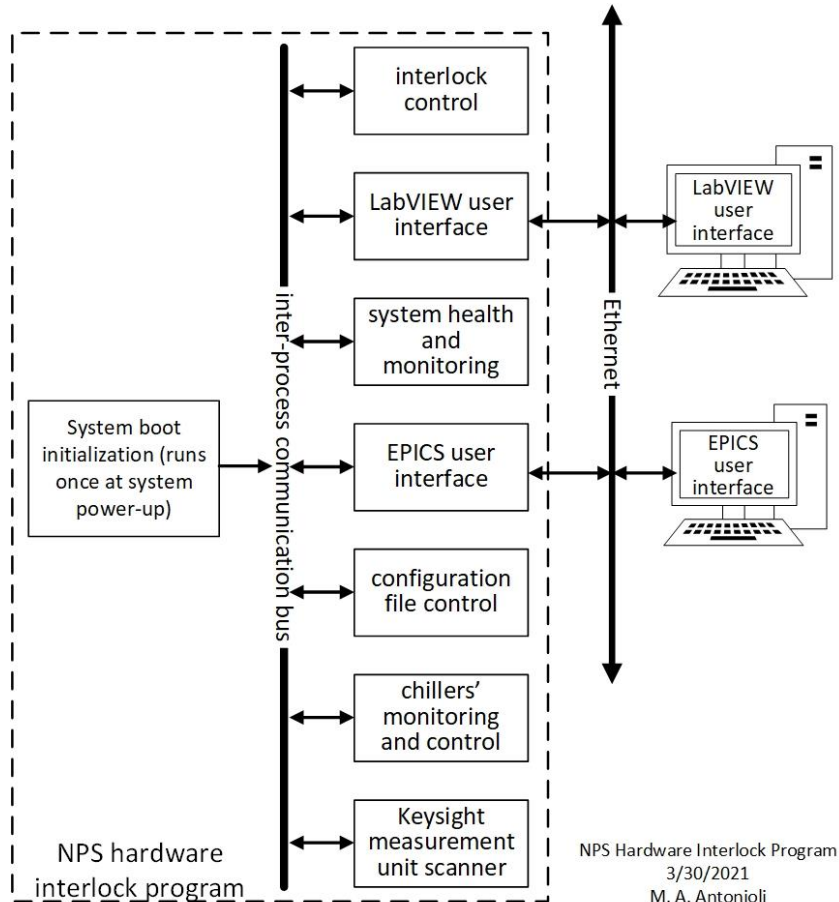
*Peter Bonneau, Aaron Brown, Brian Eng, George Jacobs, Mindy Leffel, and Marc McMullen*

1. George Jacobs is testing the first of five completed HV Supply cables
2. Marc McMullen received all SAMTEC board mount connectors needed to fabricate the test chassis

## 2. Hardware Interlock System development

*Mary Ann Antonioli, Peter Bonneau, and Aaron Brown*

1. Reviewed first draft of hardware interlock program schematic



Schematic for the NPS Hardware Interlock program

2. Mary Ann Antonioli will develop the LabVIEW front panel for the Keysight measurement unit scanner subroutine
  - Front panel will provide statistics for sensor values including average, rolling average, and standard deviation
  - Strip charts for raw data values will be included for temperature and humidity for the crystal and electronics zones
  - A heat map will be developed for the crystal zone temperatures

### 3. A7030TN stability testing strip charts

*Mary Ann Antonioli, Aaron Brown, and George Jacobs*

1. Mary Ann Antonioli completed voltage and current stability test strip charts and posted to DSG Hall C Technical Documentation webpage

### 4. VI Density plots

*Aaron Brown and George Jacobs*

1. Aaron Brown is troubleshooting issue with plot auto-save, where the axis labels and title are cut off
2. George Jacobs will generate all V and I density plots once issue is resolved
3. DSG will standardize Python versions amongst team members (3.8 is the latest version)

### 5. VLD Module

*Aaron Brown*

1. VLD module is still in prototyping stage